Assignments-2

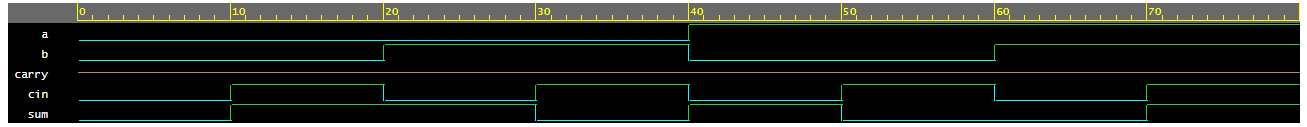
[1] Design 4-bit Ripple Carry Adder with the help of 1-bit adder.

Program :-

A screenshot of a computer

Description automatically generated

Output :-



[2] Design D-flipflop and reuse it to implement 4- bit Johnson Counter.

Program :-

A screenshot of a computer

Description automatically generated

Output :-

A screenshot of a computer

Description automatically generated

[3] Reuse 2:1 Mux code to implement 8:1 Mux.

Program :-

A screenshot of a computer

Description automatically generated

Output :-

A black screen with blue lines

Description automatically generated

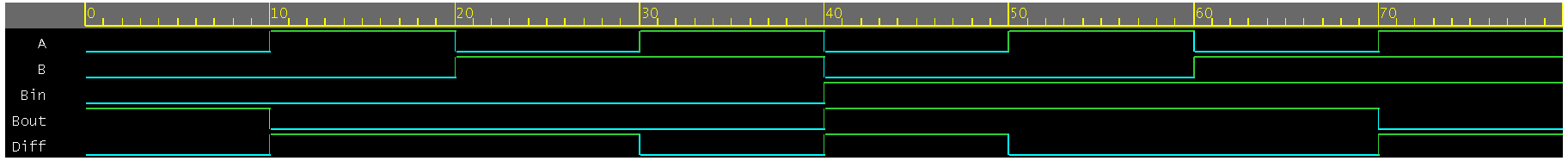
[4] Design a Full Subtractor with Gate Level Modeling Style.(use primitive gates)

Program:-

A screenshot of a computer

Description automatically generated

Output :-



[5] Design a 2X4 decoder using gate level modelling.

Program :-

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Output :-

A screenshot of a computer

Description automatically generated

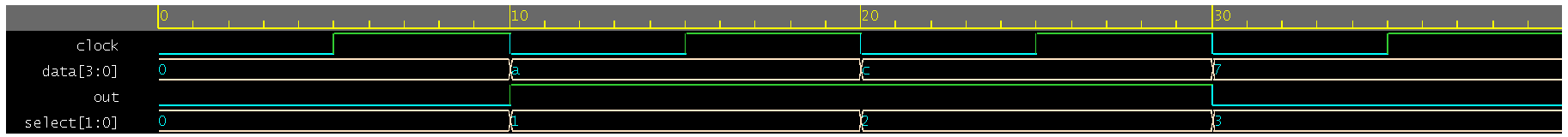
[6] Design a 4x1 mux using operators. (use data flow)

Program :-

A screenshot of a computer

Description automatically generated

Output :-



[7] Design a Full adder using half adder.

Program :-

A screenshot of a computer program

Description automatically generated

Output :-

